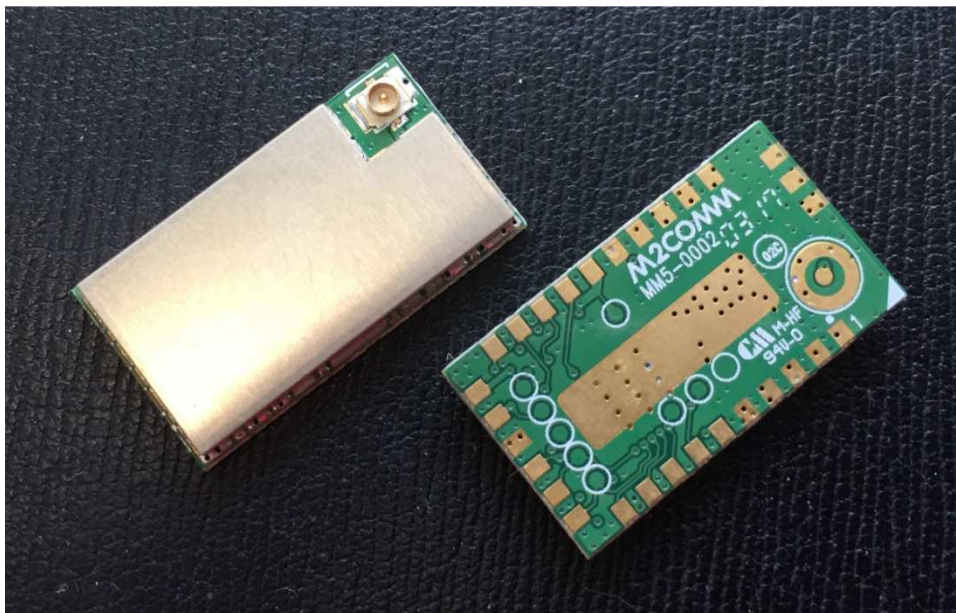




BSM8001-02-NRY

Datasheet

Elite Semiconductor Microelectronics Technology Inc.

Important Notice

All rights reserved. No part of this document may be reproduced or duplicated in any form or by any mean without the prior permission of ESMT. The contents contained in this document are believed to be accurate at the time of publication. ESMT assumes no responsibility for any error in this document, and reserves the right to change the products or specification in this document without notice. The information contained herein is presented only as a guide or examples for the application of our products. No responsibility is assumed by ESMT for any infringement of patents, copyrights, or other intellectual property rights of third parties which may result from its use. No license, either express, implied or otherwise, is granted under any patents, copyrights or other intellectual property rights of ESMT or others. Any semiconductor devices may have inherently a certain rate of failure. To minimize risks associated with customer's application, adequate design and operating safeguards against injury, damage, or loss from such failure, should be provided by the customer when making application designs. ESMT's products are not authorized for use in critical applications such as, but not limited to, life support devices or system, where failure or abnormal operation may directly affect human lives or cause physical injury or property damage. If products described here are to be used for such kinds of application, purchaser must do its own quality assurance testing appropriate to such applications.

Trademarks

ESMT and the ESMT logo are trademarks or registered trademarks of ESMT. All other company or product names mentioned herein are trademarks or registered trademarks of their respective companies.

Contact Information

Elite Semiconductor Microelectronics Technology Inc.

No.23, Industry E Rd. IV Science-Based Industrial Park,Hsinchu 300, Taiwan, R.O.C.

TEL:+886-3-5781970

FAX:+886-3-5644432

Revision Control

Ver.	Date	Description of Change	Approved
1.0	23/8/2018	Document First version	Charles Lee
1.2	06/8/2020	1. Revise company name: "Memory" → Microelectronics 2. Cover page : Revise part number 3. Revise part number in whole document. 4. Page9, Product Features, operating voltage: 3.4V~5.5V → 2.5V~5.5V 5. Page 13, VDD 3.4V → 2.5V 6. Page13, DC Current Characteristics, Sigfox Tx mode current → average current	Cindy Lu

Federal Communication Commission Interference Statement

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

FCC Caution: Any changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate this equipment.

This transmitter must not be co-located or operating in conjunction with any other antenna or transmitter.

Radiation Exposure Statement:

This equipment complies with FCC radiation exposure limits set forth for an uncontrolled environment. This equipment should be installed and operated with minimum distance 20cm between the radiator & your body.

This device is intended only for OEM integrators under the following conditions:

- I. The antenna must be installed such that 20 cm is maintained between the antenna and users, and
- II. The transmitter module may not be co-located with any other transmitter or antenna.

As long as **2** conditions above are met, further transmitter tests will not be required. However, the OEM integrator is still responsible for testing their end-product for any additional compliance requirements required with this module installed

IMPORTANT NOTE: In the event that these conditions cannot be met (for example certain laptop configurations or co-location with another transmitter), then the FCC authorization is no longer considered valid and the FCC ID cannot be used on the final product. In these circumstances, the OEM integrator will be responsible for re-evaluating the end product (including the transmitter) and obtaining a separate FCC authorization.

End Product Labeling

This transmitter module is authorized only for use in devices where the antenna may be installed such that 20 cm is maintained between the antenna and users. The final end product must be labeled in a visible area with the following:

“Contains FCC ID: 2AFXU8001UX36LDRZ24”

The grantee's FCC ID can be used only when all FCC compliance requirements are met.

Manual Information to the End User

The OEM integrator has to be aware not to provide information to the end user regarding how to install or remove this RF module in the user's manual of the end product which integrates this module.

The end user manual shall include all required regulatory information/warning as show in this manual.

Table of Contents

1	<i>Product Overview.....</i>	9
2	<i>Product Features</i>	9
3	<i>Functional Block.....</i>	10
4	<i>Pin Assignment and Package Mechanicals.....</i>	11
5	<i>Electrical Specifications</i>	13
6	<i>Preloaded Software</i>	16
6.1	<i>Special Notes for Sigfox AT Command Interface.....</i>	16
6.2	<i>Bootloader.....</i>	16
6.3	<i>Device ID, KEY and Portable Access Code.....</i>	17
7	<i>Transmission Power and Current Consumption.....</i>	18
7.1	<i>Input Voltage vs. Transmission Power vs. Current.....</i>	18
7.2	<i>Current Profile and Measurement.....</i>	19
8	<i>Application Information.....</i>	20
8.1	<i>Recommended Connection to Essential Pins.....</i>	20
8.2	<i>Switch ON and OFF Procedure</i>	21
8.3	<i>RF Grounding for FCC Compliance.....</i>	21
9	<i>Certification.....</i>	22
10	<i>Order Information.....</i>	22

List of Figures

Figure 3-1 Module Block Diagram.....	10
Figure 4-1 Bottom View of Module.....	11
Figure 4-2 Module Pinout.....	12
Figure 7-1 Current Profile.....	19
Figure 8-1 Typical Application Circuit.....	21

List of Tables

Table 5-1 Absolute Maximum Ratings	13
Table 5-2 Recommended Operating Conditions.....	13
Table 5-3 DC Current Characteristics	13
Table 5-4 Transmitter RF Performance	13
Table 5-5 32kHz RC Oscillator Specification.....	14
Table 5-6 Crystal Oscillator Specification	14
Table 5-7 Pin IO Voltage	15
Table 7-1 Recommended Pin Connections	18
Table 8-1 Recommended Pin Connections	20

1 *Product Overview*

The BSM8001-02-NRY is a Sigfox Verified modem module for the low power wide area network (LPWAN) market. It is designed with ESMT's Uplynx XS8001 SOC. The module was designed for high performance, high quality, small form factor and a low cost. The design is fully compliant with FCC regulations. The Sigfox application is ported over the Uplynx XS8001 and executed at high efficiency using its internal 32bit core processor. Every module is preloaded with Sigfox application software and module specific ID/KEY/PAC as specified by the Sigfox network system. The preloaded software also includes a bootloader which allows software updates or future user application development.

2 *Product Features*

- Sigfox Verified™ RF modem (reference design fully certified)
- Frequency range: ISM 915MHz (902-928MHz)
- Maximum output power: 22dBm typical (Complaint to FCC part 15.247)
- Current consumption for packet transmission: 130mA at 22dBm@902MHz
- Operating Voltage: 2.5V to 5.5V
- Operating Temperature: -40°C to 85°C
- Module enabling pin (POW_EN)
- 0.05µA OFF current
- LGA 29 24mm x 13.5mm (RF IPEX connector) Land Grid Array

3 Functional Block

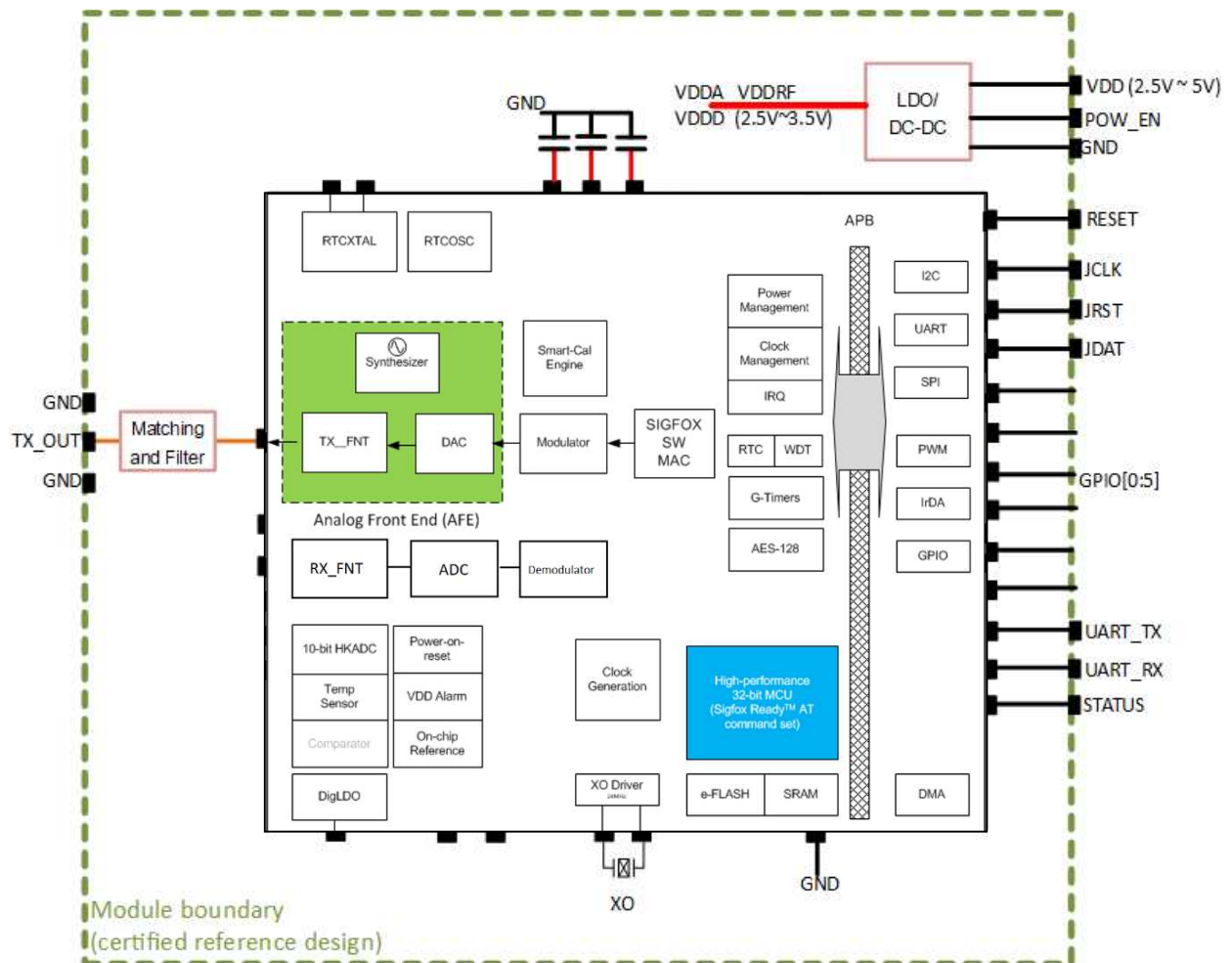


Figure 3-1 Module Block Diagram

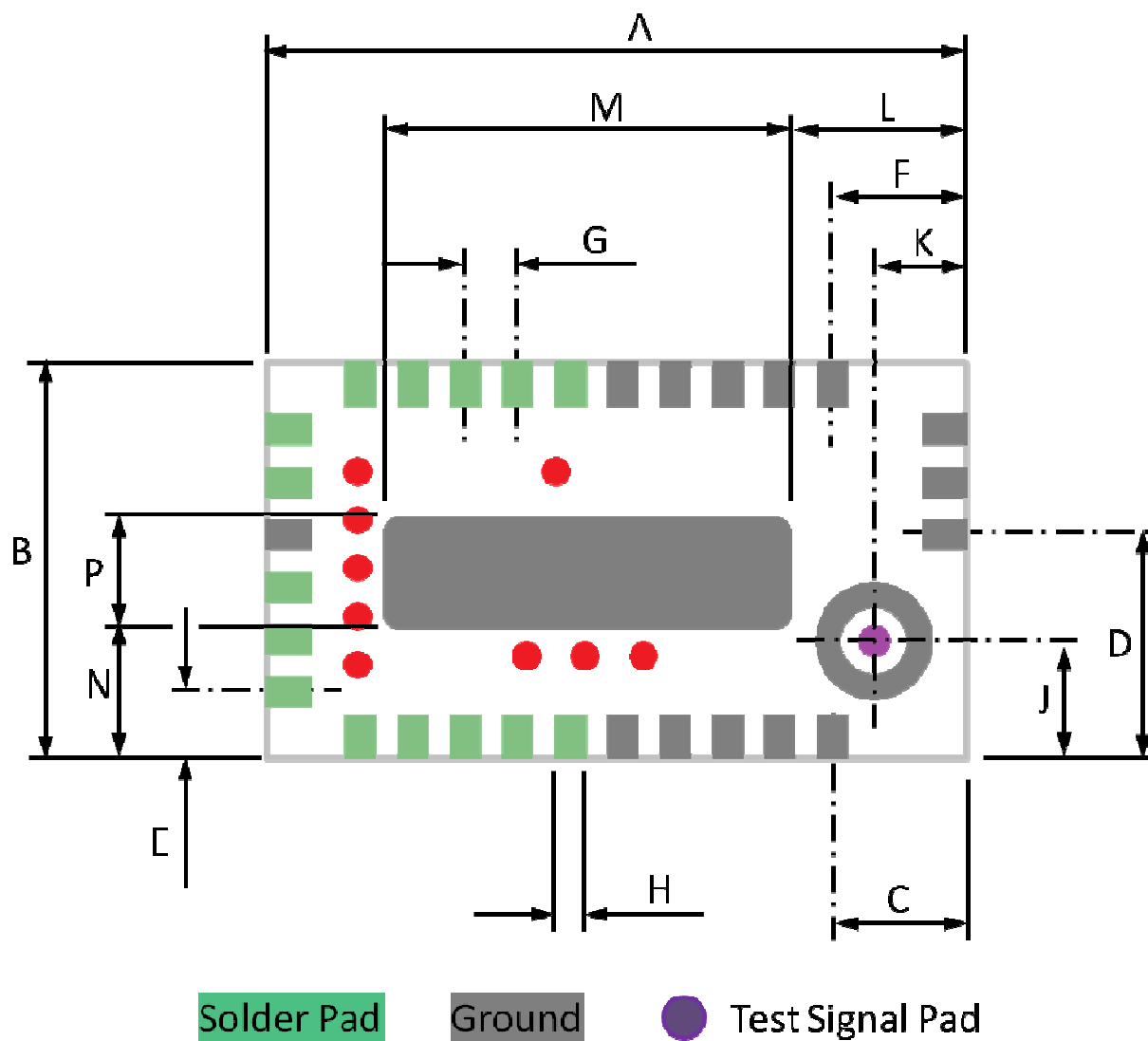
The core of the BSM8001-02-NRY module is the Uplynx XS8001 SOC. The module design is based on the XS8001 Sigfox Verified™ reference design. The BSM8001-02-NRY has 6 sets of analogue/digital multi-function pins and digital only multi-function pins. Each multi-function pin can be configured by the user.

The BSM8001-02-NRY communicates with the host MCU over a UART interface. The preloaded UART interface firmware is configured at 9600bps baud rate, 8-bit data, no parity bit, 1 stop bit and no flow control.

The STATUS pin indicates the activity of the Sigfox AT command interface. The module is switched ON and OFF with the POW_EN pin. In OFF mode, the module current consumption can be cut to its minimum (0.05μA) for longer battery operating time, an essential requirement for Sigfox where modem activity is very low.

The RF output is a 50Ω IPEX connector and the whole module is shielded for best spurious containment.

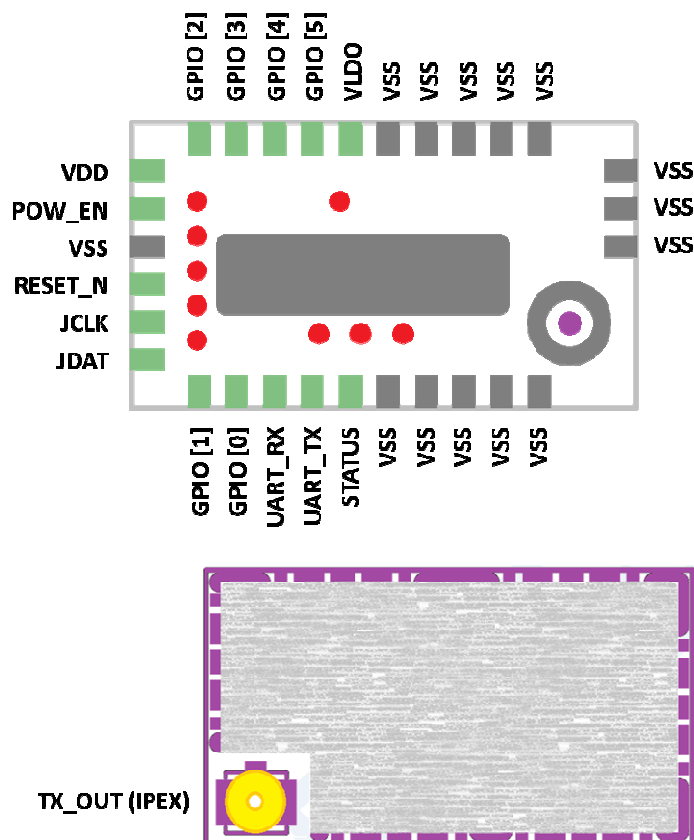
4 Pin Assignment and Package Mechanicals



Dimension	A	B	C	D	E	F	G
mm	24	13.5	4.6	7.65	2.25	4.6	1.8 TYP.
Dimension	H	J	K	L	M	N	P
mm	1.0 TYP.	4.1	3.3	5.9	14	4.5	3.9
Pad Length: 1.68 TYP.							
Maximum thickness: 3.0							

Figure 4-1 Bottom View of Module

PCB Layout and Gerber files are available upon request



Pad Name	IO	Pin function with AT command	Pin definition with SDK (XS8001 pin)
VDD	VDD	Power	
VSS	GND	GND	
VLDO	0	Operating voltage monitor point	
RESET_N	I	Reset: When asserted LOW sets module to INITIAL state	
POW_EN	I	Enable: Logic low for Disable; Logic high for Enable	
UART_TX	O	UART Tx data (9600bps) to host UART receiver	UART0_TX or GPIO19 [pin17]
UART_RX	I	UART Rx data (9600bps) to host UART transmitter	UART0_RX or GPIO20 [pin18]
GPIO[0]	IO	Digital GPIO[0]	I2C_CLK or GPIO21 [pin19]
GPIO[1]	IO	Digital GPIO[1]	I2C_SDA or GPIO22 [pin20]
GPIO[2]	IO	Digital GPIO[2]	SPI0_MOSI or GPIO25 [pin35]
GPIO[3]	IO	Digital GPIO[3]	SPI0_MISO or GPIO26 [pin36]
GPIO[4]	IO	Digital GPIO[4]	SPI0_CLK or GPIO27 [pin37]
GPIO[5]	IO	Digital GPIO[5]	SPI0_CS or GPIO28 [pin38]
STATUS/ Bootloader	IO	Logic High: Module ready Logic Low: Module busy	GPIO2 [pin16]
		(BOOTLOADER is enabled when STATUS pin is pulled low by application circuit during power ON)	
JDAT	I	JTAG Interface DAT	
JCLK	I	JTAG Interface CLK	
RF_IO	0	50Ω antenna output	

Figure 4-2 Module Pinout

5 Electrical Specifications

Parameter	Min	Max	Unit
Power (VDD)	-0.3	5.5	V
Voltage on GPIO	-0.3	3.6	V
Storage Temperature	-40	140	°C
Maximum soldering temperature		250	°C

Table 5-1 Absolute Maximum Ratings

Parameter	Min	Max	Unit
VDD	2.5	5.5	V
Operating Temperature	-40	85	°C

Table 5-2 Recommended Operating Conditions

Parameter	Min	Typ.	Max	Unit
Off mode		0.05	1	μA
Average current 600bps packet at 902/920MHz		130		mA

Table 5-3 DC Current Characteristics

Parameter	Min	Typ.	Max	Unit
Output power		22	23	dBm
Harmonics conducted 915MHz 22dBm output			FCC Compatible	dBm
Output Power deviation vs Input voltage (2.6V ~5.5V)			1	dB

Table 5-4 Transmitter RF Performance

Parameter	Note	Min	Typ.	Max	Unit
Calibrated frequency	±5% course calibration		32.768		kHz
Frequency accuracy after calibration	With software offset adjustment routine			±1	%
Supply voltage coefficient	Frequency drift when supply voltage changes after calibration		+10		%/V
Initial calibration time			2.5		ms

Table 5-5 32kHz RC Oscillator Specification

Parameter	Condition/Note	Min	Typ.	Max	Unit
Crystal Frequency			12	24	MHz
Crystal Frequency accuracy requirement	This is the total tolerance including; a) initial tolerance b) crystal loading c) aging d) temperature dependence The acceptable crystal tolerance depends on RF frequency and channel spacing / bandwidth		±20		ppm
Startup time				2	ms

Table 5-6 Crystal Oscillator Specification

Parameter	Min	Typ.	Max	Unit
Input Low Voltage	-0.3		0.8	V
Input High Voltage	2		3.6	V
Threshold point	1.36	1.45	1.55	V
Output High Voltage	2.4			V
Output Low Voltage			0.4	V
Schmitt Trigger Low to High Threshold Point	1.56	1.66	1.76	V
Schmitt Trigger High to Low Threshold Point	1.1	1.19	1.27	V
Input Leakage Current			±10	μA
Pull up resistor	42k	59k	88k	Ω
Pull down resistor	34k	54k	92k	Ω
Output current drive			20	mA

Table 5-7 Pin IO Voltage

6 Preloaded Software

The BSM8001-02-NRY is loaded with the following software prior shipping:

1. Sigfox Verified™ Application
2. Bootloader
3. Device ID, KEY and Portable Access Code (PAC)

For Sigfox Verified AT command interface, details can be found in;
ESUG-UPLYNX-001: Uplynx AT Command GUI and EasyAT Users Manual

For Software development on module, the features of the bootloader can be found in;
ESAN-UPLYNX-003: Uplynx Products (Addendum - Boot Procedure)

6.1 Special Notes for Sigfox AT Command Interface

- To load the Sigfox library for specific region X, one should set (AT\$RCZ) and confirm (AT\$RCZ?) the value stored for RCZ then execute AT\$O=1,X
- The configuration set by the following AT commands will be stored on flash and the value will be retained even after power off
 - o Transmitting frequency (i.e. AT\$IF)
 - o Transmitting power (i.e. AT\$302)
 - o Sigfox configuration word (i.e. AT\$400)
 - o The following setting **must be loaded** to ensure normal Sigfox network activity:
 - **RCZ2: AT\$IF=902200000, AT\$400=1,0,0,1**
 - **RCZ4: AT\$IF=902200000, AT\$400=0,40000000,0,63**
 - o Set RCZ region by AT\$RCZ

6.2 Bootloader

The preloaded bootloader allows the user to reprogram the flash in the SOC. To enter the firmware update mode, user can

- I. Pull pin 16 low at startup (i.e. STATUS pin of module)
On start up, the bootloader polls the module “STATUS” pin which is the pin 16 of the SOC. If the pin is logically low, the UART on the SOC is configured as 115200bps and the bootloader is waiting for firmware via XMODEM. User can then upload the application binary file to the SOC via XMODEM. Details can be found in “ESUG-UPLYNX-001: Uplynx AT Command GUI and EasyAT Users Manual”. The application will be stored at the application startup address and be loaded up after the system is rebooted. Details can be found in the “ESUG-UPLYNX-004: Uplynx Software Development Kit Users Manual”. Since the “STATUS” pin is an output pin at normal operation, it is important to ensure the pin is NOT pulled down by the application circuit during normal startup.
- II. Enter AT\$FW=1 when the Sigfox verified AT command interface is uploaded, the module enters XMODEM mode after the AT\$FW command is asserted. The speed of UART is configurable to 115200bps. The new firmware is to be transmitted via XMODEM protocol over UART and will be installed automatically. Upon successful update, the new firmware will be loaded on the next reboot. Details can be found in “ESUG-UPLYNX-001 Uplynx AT Command GUI and EasyAT Users Manual”

6.3 *Device ID, KEY and Portable Access Code*

As part of the Sigfox operation requirements, each Sigfox device must be assigned a unique identification number (ID), encryption key (KEY) and portable access code (PAC). This information is preloaded in the module and only the ID and PAC can be read via AT command.

7 Transmission Power and Current Consumption

7.1 Input Voltage vs. Transmission Power vs. Current

The Uplynx XS8001 operates from 1.8V to 3.6V and the module is designed to leave enough headroom for battery voltage variation and also provide maximum RF transmission power.

3.6V is selected.

With AT\$302, the module transmission power is configured within the range of 14 to 22.

The transmission power and current consumption are measured and are shown below. The user can select an appropriate power level setting to accommodate losses.

902.2MHz		
VDD	Power	Current (Continuous Wave)
5	22.3	170.6
4.5	22.3	170.6
4	22.3	170.6
3.9	22.3	170.6
3.8	22.3	170.6
3.7	22.3	170.6
3.6	22.3	170.6
3.5	22.3	170.6
3.4	22.3	170.6
3.3	22.16	167.5
3.2	21.96	163.4
3.1	21.75	159.6
3	21.53	155.9
2.9	20.31	152.5
2.8	21.06	149.3
2.7	20.8	146.1
2.6	20.5	142
2.5	20.1	135.8

Table 7-1 Recommended Pin Connections

The current consumption of the BSM8001-02-NRY is measured and data is presented as a reference. The current is measured using the evaluation kit for BSM8001-02-NRY. Current through J2 is measured by a Tektronix MDO3104 and a TCPA300. The module is configured for RCZ2 and AT\$SB=1 is executed to send a bit.

7.2 Current Profile and Measurement

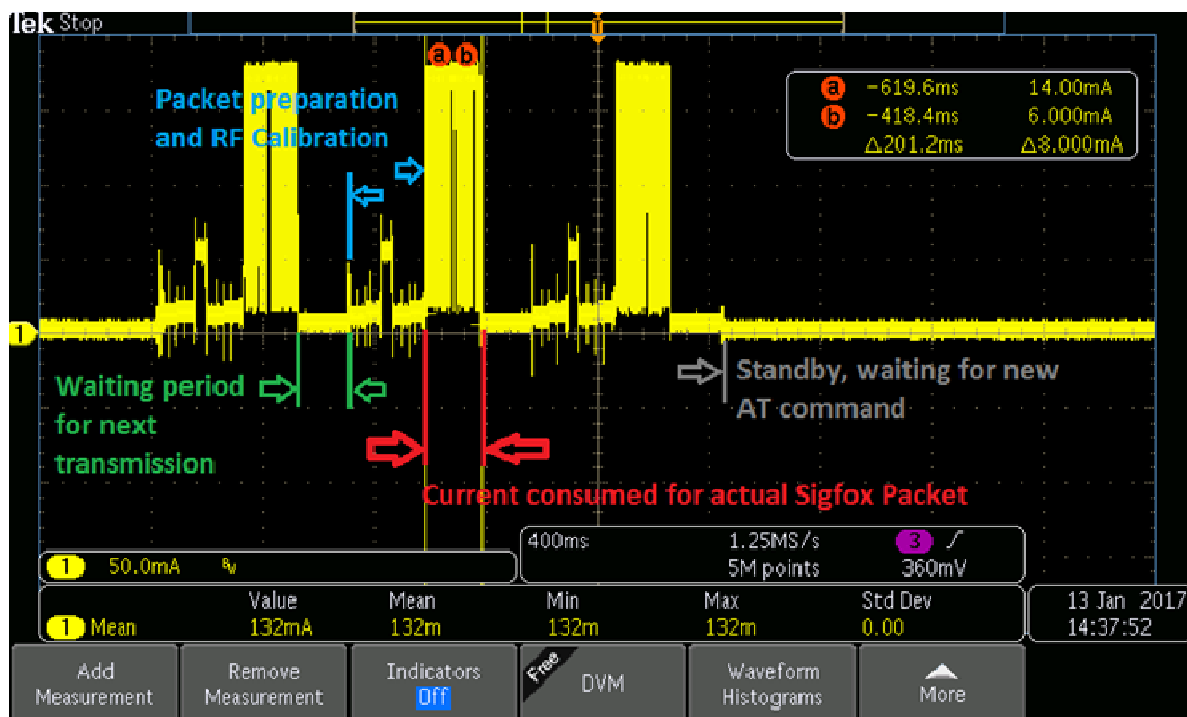


Figure 7-1 Current Profile

VDD @ 5V, 902.2MHz	
Output Power	Current
AT\$302 to give	Average Current consumption AT\$SB=1
14dBm	59.3mA
16dBm	70.6mA
18dBm	83mA
20dBm	124mA
22dBm	132mA
Standby Current (waiting for AT command)	2.5mA
Average current between packets (waiting period + calibration + packet preparation)	14.5mA

8 Application Information

8.1 Recommended Connection to Essential Pins

PIN	Recommendation
TX_OUT	The 50Ω RF output should be connected to a pi/T antenna matching circuit for potential antenna tuning. An ESD diode could be placed at the antenna port to protect the product against an ESD event induced due to human activity.
VSS	The RF performance relies strongly on a good quality ground. It is recommended to use a ground plane if possible or thick and close ground traces are required to minimize radiation and ensure the best RF performance
STATUS	Can be connected to the host processor to detect module status or the pin can lead to a status LED for displaying purposes (This pin must not be pulled down during power up or the unit will enter firmware update mode)
VDD	The voltage supply can come directly from a cell, ac adapter or USB. A decoupling capacitor of size 10μF placed close to the 5V input is recommended. The allowable voltage at this power input is below 5.5V and above 2.5V.
POW_EN	It is connected to a host processor to switch on and off the modem module.
RST_N	This can be tied to a universal reset pin or the host processor. It is internally pulled high to VLDO. In other words, RST_N only works when POW_EN is set high. A 10μF capacitor would be recommended in place close to the module pin in case the supply voltage to the module. The pull high resistance onto pin RESET_N from the system must be smaller than 50kΩ to ensure proper module startup.
VLDO	This is the output of the internal LDO of the module. This voltage supply is used by the module RF system and it is highly recommended to leave this pin unconnected. In case this supply is used by another part of the system, a 1μF capacitor is recommended to be put close to the module.
UART_TX / UART_RX	When module is OFF, pin must not be asserted HIGH. Isolation circuit is recommended to avoid current leakage.
GPIO[0:5]	When module is OFF, pin must not be asserted HIGH. Isolation circuit is recommended to avoid current leakage.

Table 8-1 Recommended Pin Connections

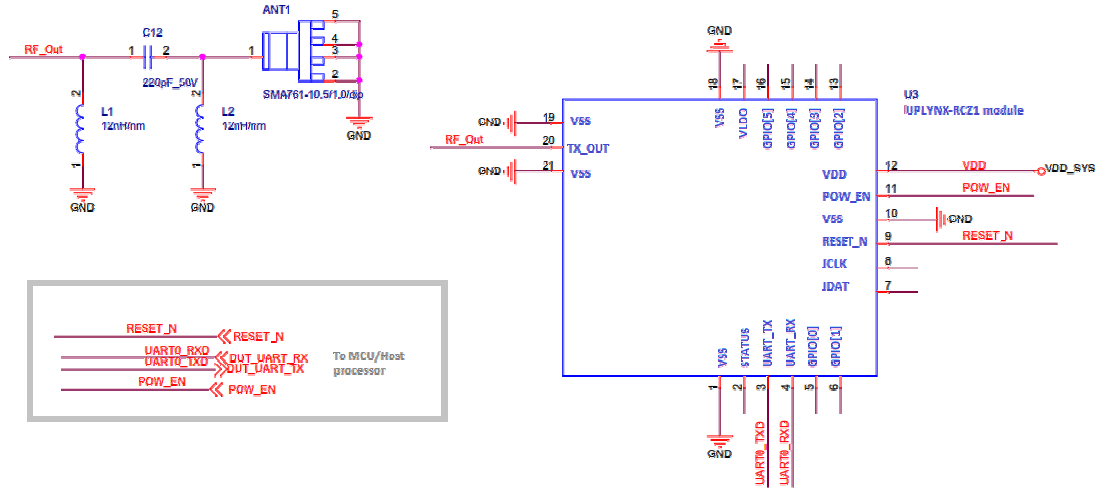


Figure 8-1 Typical Application Circuit

8.2 Switch ON and OFF Procedure

To ensure proper operation, POW_EN must be asserted high after 5V has settled then wait for 5ms before AT commands can be passed down the UART interface. The settling time is required for the LDO and crystal oscillator to settle to their operating conditions. A software routine can be used to poll the status of STATUS pin before any actual payload is passed to the module.

After switching the module OFF, by pulling POW_EN low, it is highly recommended that the system should wait for at least 10ms before another attempt to power ON.

8.3 RF Grounding for FCC Compliance

The BSM8001-02-NRY is designed to deliver ~22dBm and is compliant to FCC emission regulations. To achieve the tight emission limitation of the restricted bands, the grounding of the module must be maintained by connecting all the ground pads to a large ground plan in the PCB stack. It is highly recommended that the mother board be designed with a dedicated ground plane which connects to all the VSS pins on the module. The VSS pads must be connected to the ground plane with a minimum of 3 vias. Decoupling capacitors must be located close to the module supply voltage. To maximize heat dissipation, the large ground pad must be connected to the “mother” PCB. Special care must be paid to the solder mask on the mother board to ensure highest yield.

9 Certification

- Sigfox RCZ24 reference design certification
- FCC Part 15.247 (FCC ID 2AFXU8001UX36LDRZ24)

10 Order Information

Part Number	Description
BSM8001-02-NRY	Uplynx Sigfox Verified RCZ24 modem module

Related products

Part Number	Description
BSE8001-02-NRY	Uplynx Sigfox Verified RCZ24 modem Evaluation Kit